

PATENT
450100-05079

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Takashi YOKOKAWA et al.
International Application No.: PCT/JP04/005562
International Filing Date: April 19, 2004
For: DECODING APPARATUS, DECODING METHOD,
AND PROGRAM

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Enclosed are copies of the publications, which were cited in the International Search Report of International Application No. PCT/JP04/005562. Also enclosed is a copy of Form PTO-1449 and the International Search Report.

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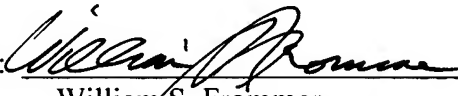
REMARKS

Entry of this Information Disclosure Statement and an early examination on the merits are respectfully solicited.

Please charge any additional fees to Deposit Account No. 50-0320.

Respectfully submitted,

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Enclosures

Based on Form PTO-1449 (3/90)	ATTY. DOCKET NO. 450100-05079	SERIAL NO. 521054 Filed Concurrently Herewith
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LIST OF REFERENCES CITED BY APPLICANT
(Use several sheets if necessary)

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	6, 539, 367	03/25/03	Blanksby et al.			
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	AL	JP 2003-269302	09/25/03	Japan			
	AM	JP 2002-33670	01/31/02	Japan			
	AN	EP 1 158 682	11/28/01	Europe			
	AO						
	AP						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	AQ		S. Kim et al, 'Parallel VSLI Architectures for a Class of LD PC Codes', IEEE International Symposium on Circuits and Systems, 2002, ISCAS 2002, Volume 2, November 2002, p. 26-29				
	AR						
	AS						

EXAMINER	DATE CONSIDERED
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